	Application No.	Applicant(s)	
Notice of Allowability	10/038,942	SHAH ET AL.	
	Examiner	Art Unit	
	Nitin C. Patel	2116	
The MAILING DATE of this communication appeal All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIOF of the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED or other appropriate committee GHTS. This application is	in this application. If not included nunication will be mailed in due cour	rse. THIS
1. $\boxtimes$ This communication is responsive to <u>amendment filed on 1</u>	4 February 2005.		
2. X The allowed claim(s) is/are 1-3,5-12 and 14-22.			
3. $\boxtimes$ The drawings filed on <u>31 December 2001</u> are accepted by	the Examiner.		
4. ☐ Acknowledgment is made of a claim for foreign priority una) ☐ All b) ☐ Some* c) ☐ None of the:  1. ☐ Certified copies of the priority documents have 2. ☐ Certified copies of the priority documents have 3. ☐ Copies of the certified copies of the priority documents have international Bureau (PCT Rule 17.2(a)).  * Certified copies not received:  Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONM THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.	been received. been received in Applicat cuments have been receiv	ion No ed in this national stage application	
5. A SUBSTITUTE OATH OR DECLARATION must be subm INFORMAL PATENT APPLICATION (PTO-152) which give			CE OF
<ol> <li>CORRECTED DRAWINGS (as "replacement sheets") must (a) including changes required by the Notice of Draftspers         <ol> <li>hereto or 2) to Paper No./Mail Date</li> <li>including changes required by the attached Examiner's Paper No./Mail Date</li> <li>Identifying indicia such as the application number (see 37 CFR 1 each sheet. Replacement sheet(s) should be labeled as such in the sheet. Replacement sheet(s) should be labeled as such in the deposit of the saminer's comment regarding REQUIREMENT</li> </ol> </li> </ol>	on's Patent Drawing Revie s Amendment / Comment on .84(c)) should be written on the header according to 37 Co sit of BIOLOGICAL MA	or in the Office action of the drawings in the front (not the bac FR 1.121(d). FERIAL must be submitted. Note	
Attachment(s)  1. ☐ Notice of References Cited (PTO-892)  2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948)  3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/0 Paper No./Mail Date  4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material	6. ☐ Interview Paper No. 7. ☐ Examiner	nformal Patent Application (PTO-15 Summary (PTO-413), ./Mail Date s Amendment/Comment s Statement of Reasons for Allowan 	

Application/Control Number: 10/038,942 Page 2

Art Unit: 2116

## **DETAILED ACTION**

1. This is in responsive to amendment filed on 14 February 2005.

- 2. Claims 4, and 13 have been cancelled.
- 3. Claims 1 3, 5 12, and 14 22 are allowed.

## **REASONS FOR ALLOWANCE**

The following is an examiner's statement of reasons for allowance:

Applicant's claimed invention distinguishes over the prior art for following reasons. The independent claims 1, and 10 are allowable over the art of record and none of references, either alone or in combination, discloses or renders obvious a system with an interface circuit arrangement for coupling the first and second logic circuit with a multiplexer to receive the incoming data stream and a first register coupled to the multiplexer, which is clocked by the first clock; wherein the multiplexer provides the first register the inverted version of the output of the first register when the incoming data stream includes the first asserted signal, the first register provides the intermediate signal, and an output logic block coupled to the input logic block to receive the intermediate signal and a second clock, wherein the output logic block provides to the second logic circuit an output signal, the output signal is a second asserted signal for one clock period of the second clock, when the output logic block detects a logic state change in the intermediate signal as described in claimed invention.

The independent claims 19 is allowable over the art of record and none of references, either alone or in combination, discloses or renders obvious a method for coupling the first and second logic circuit with steps of providing an input data stream

from a first logic circuit clocked by a first clock to a first register, ascertaining a first asserted signal in the input data stream, creating an intermediate signal by inverting the output of the first register when the first asserted signal is received, comparing the logic states of the intermediate signal at different times, the different times equal to a first time period of a second clock, and outputting a second asserted signal to a second logic circuit for a period equaled to the second clock when the logic states of the intermediate signal are different during the first time period as described in claimed invention.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

## Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Nitin C. Patel whose telephone number is 571-272-3675. The examiner can normally be reached on 6:45 am to 5:15 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Lynne H. Browne can be reached on 571-272-3670. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Application/Control Number: 10/038,942 Page 4

Art Unit: 2116

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Nitin C. Patel March 8, 2005

LYNNE H. BROWNE SUPERVISORY PATENT EXAMINER TECHNOLOGY CENTER 2100